

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Customer Number: 20277

PATENT

Fumitoshi YAMAMOTO, et al.

Confirmation Number:

Serial No.: 10642214

Group Art Unit:

Filed: August 18, 2003

Examiner:

For:

SEMICONDUCTOR DEVICE WITH SURGE PROTECTION CIRCUIT

CORRECTED CLAIM OF PRIORITY AND TRANSMITTAL OF CERTIFIED PRIORITY DOCUMENT

Mail Stop CPD Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 35 U.S.C. 119, Applicants hereby claims the priority of:

Japanese Patent Application No. JP2002-368456, Filed on December 19, 2002

cited in the Declaration of the present application.

Respectfully submitted,

AT Jallana

MCDERMOTT, WILL & EMERY

Gene Z. Rubinson

Registration No. 33,351

600 13th Street, N.W. Washington, DC 20005-3096

(202) 756-8000 GZR:gav Facsimile: (202) 756-8087

Date: December 2, 2003